

FIG. 1

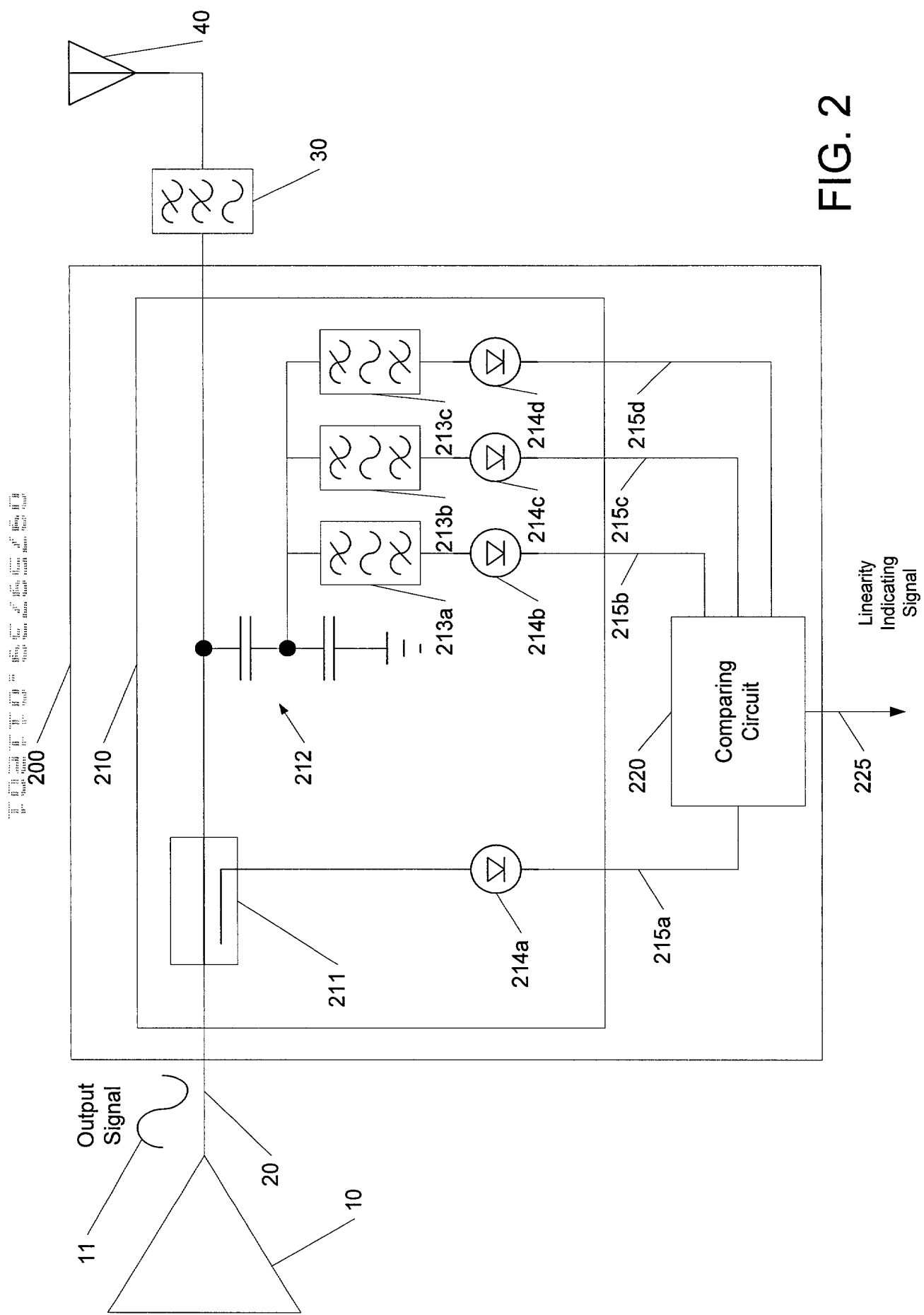


FIG. 2

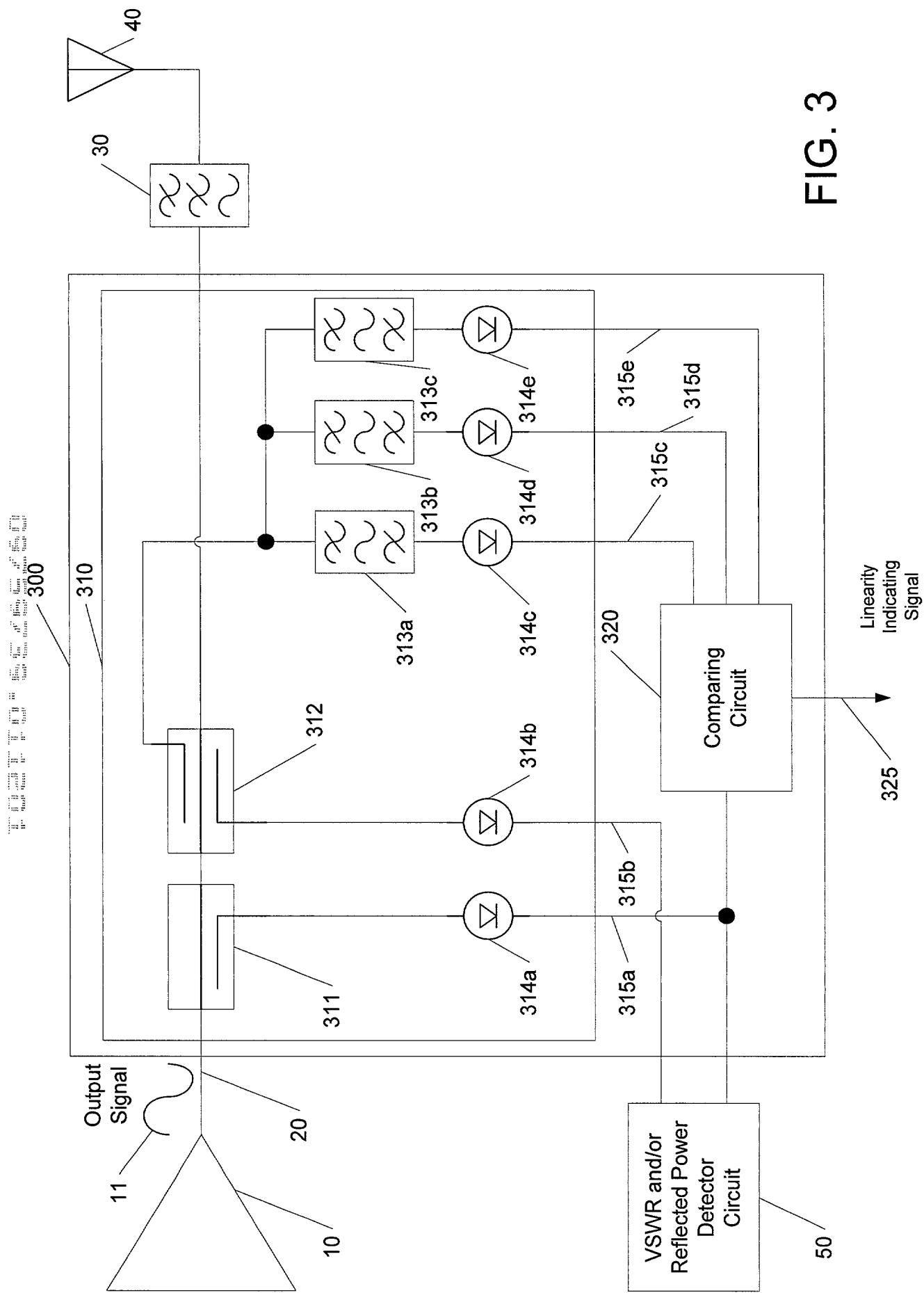


FIG. 3

FIG. 4 is a block diagram of a system 400 for measuring the linearity of a device 10. The system 400 includes an input signal source 30, a device 10, an output signal source 40, a VSWR and/or Reflected Power Detector Circuit 50, a Comparing Circuit 420, and a Linearity Indicating Signal output 425. The device 10 is connected to the input signal source 30 and the output signal source 40. The VSWR and/or Reflected Power Detector Circuit 50 is connected to the output signal source 40. The Comparing Circuit 420 is connected to the VSWR and/or Reflected Power Detector Circuit 50 and the input signal source 30. The Linearity Indicating Signal output 425 is connected to the Comparing Circuit 420.

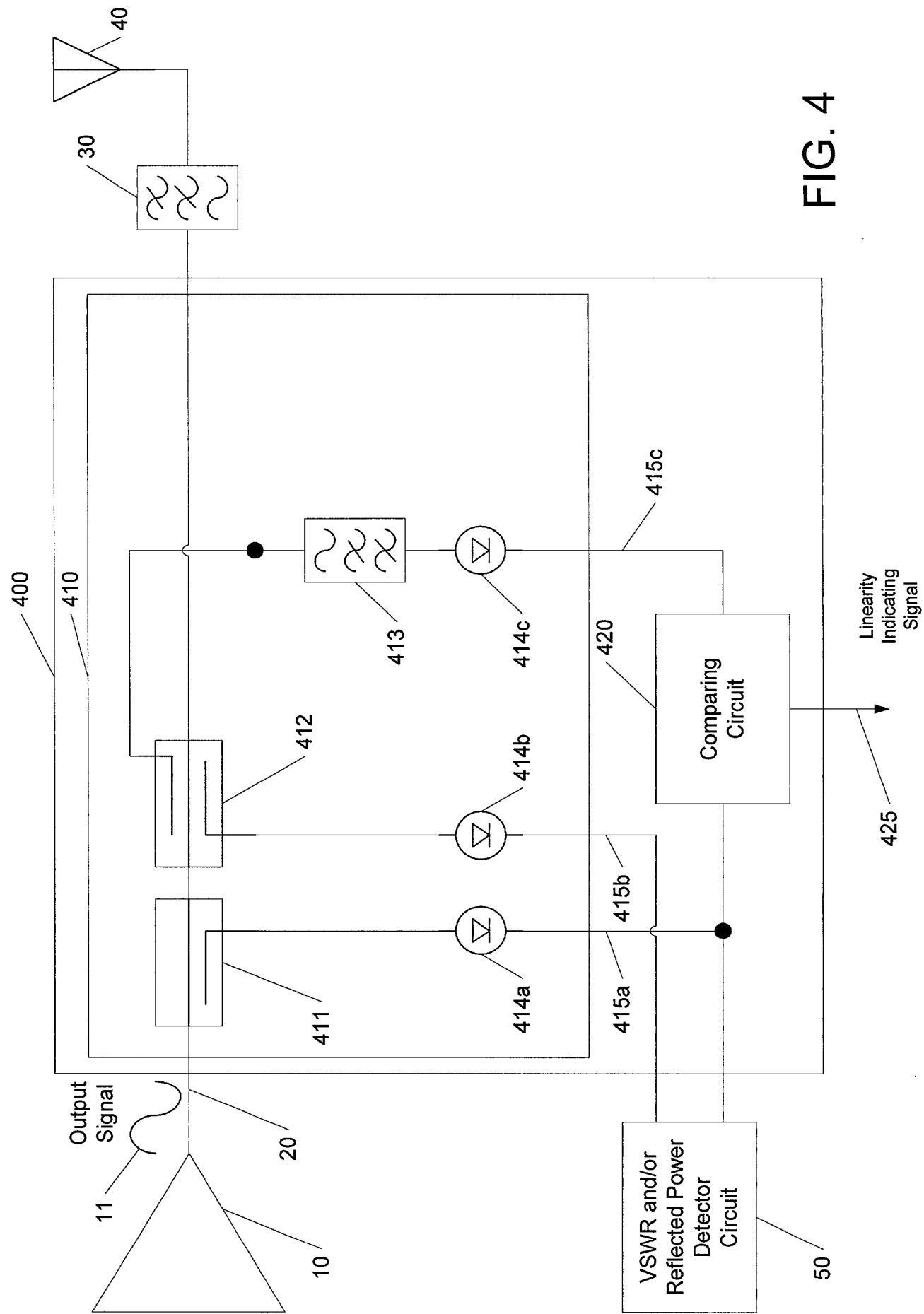


FIG. 4

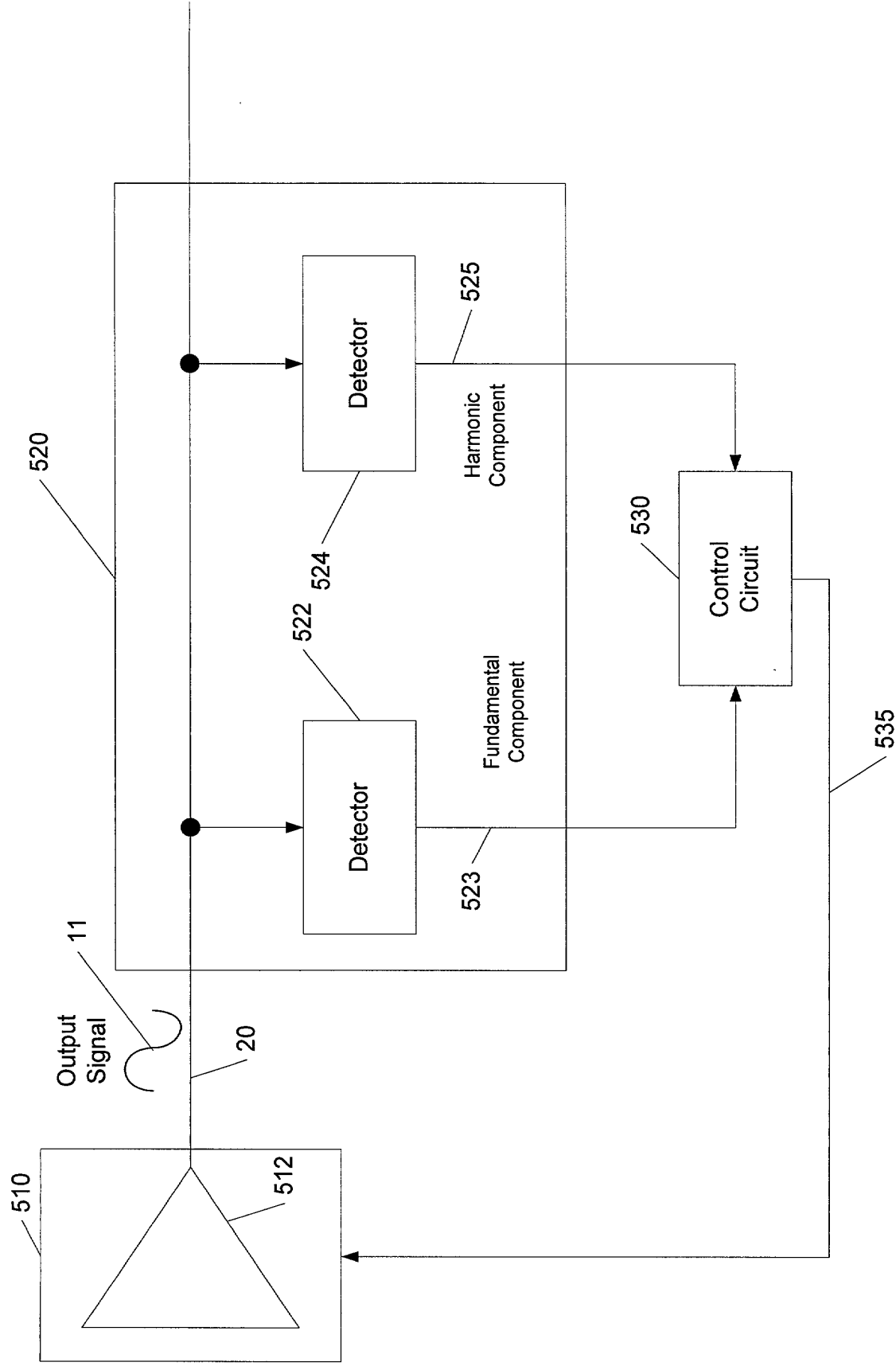


FIG. 5

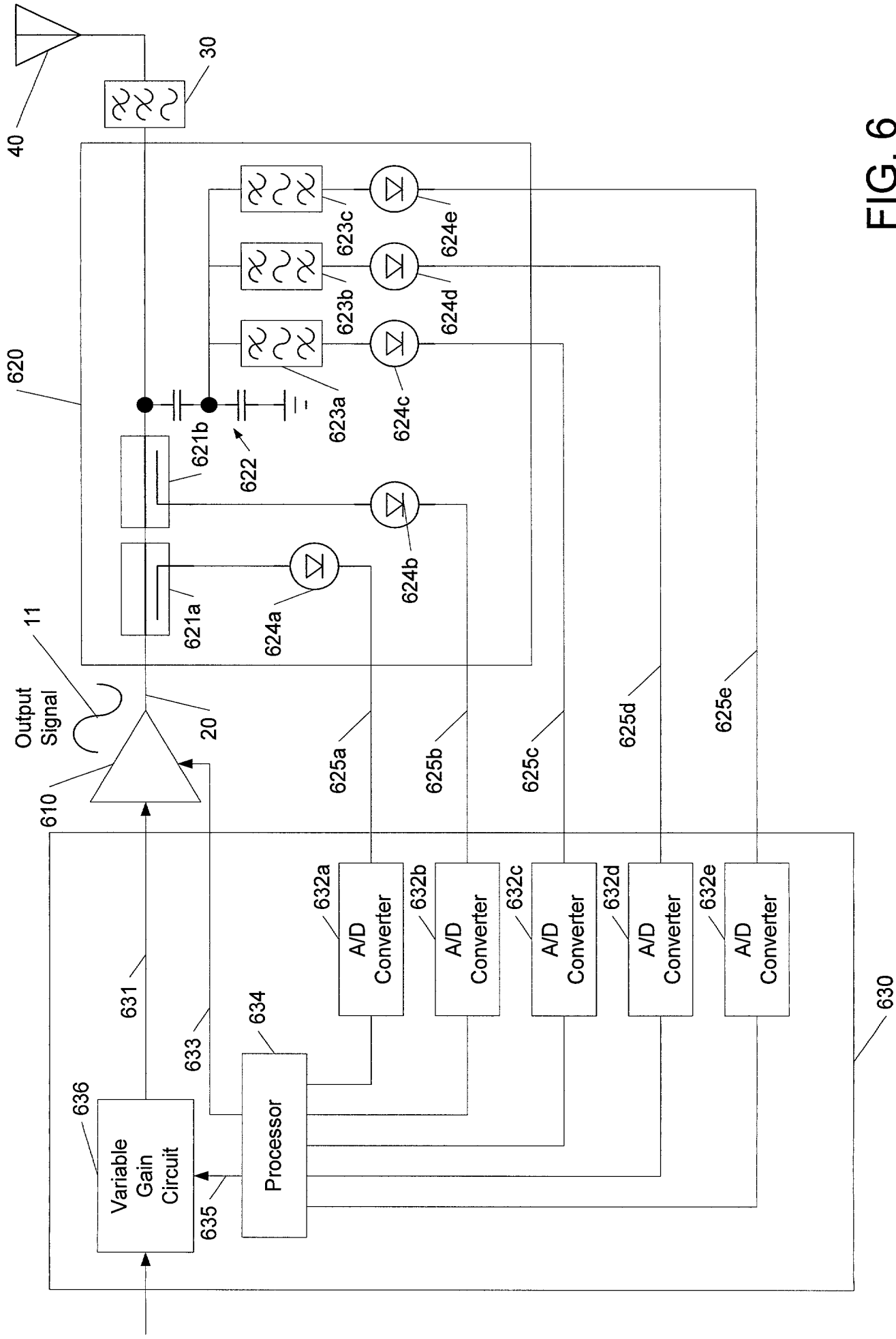
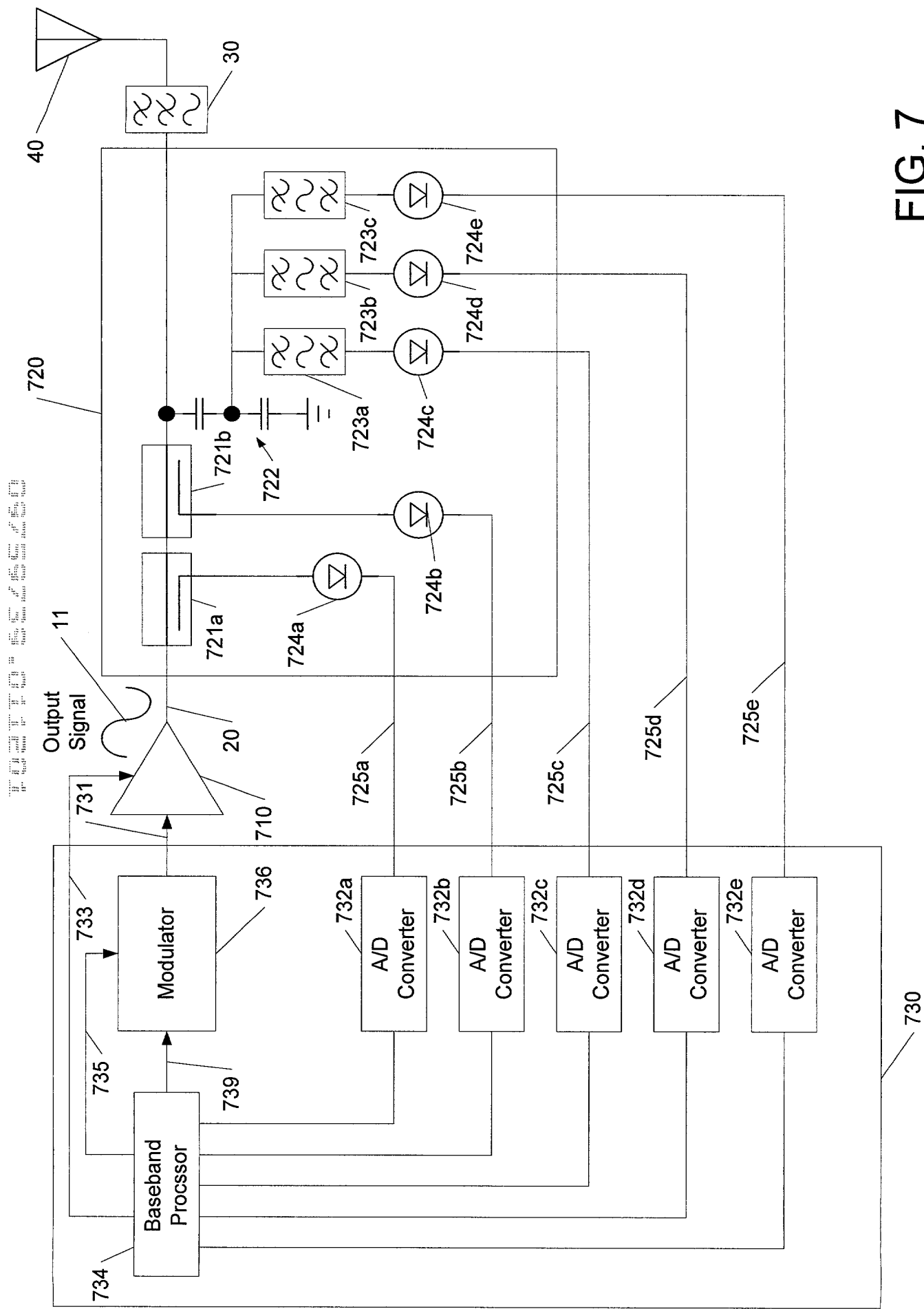


FIG. 6



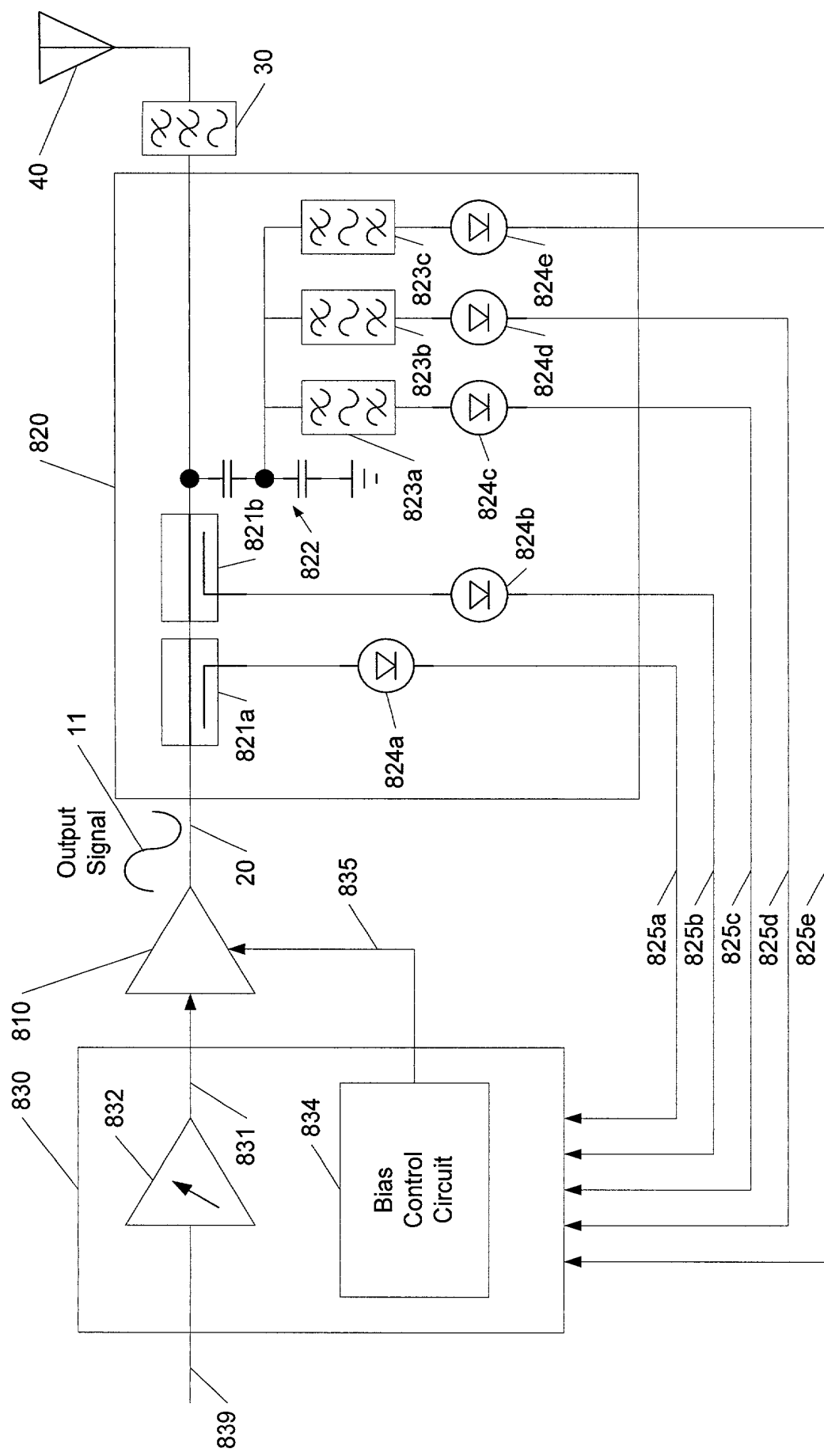


FIG. 8



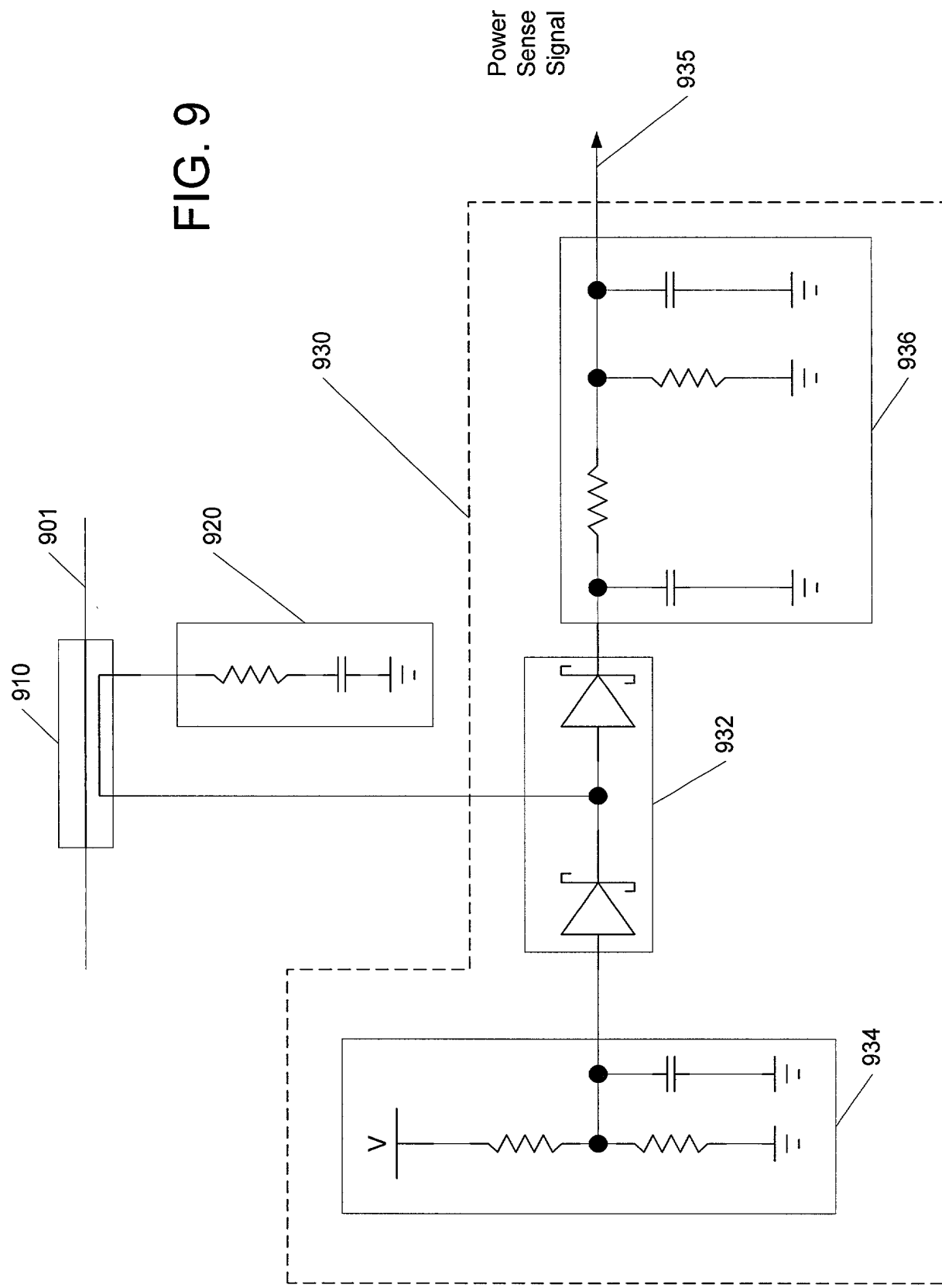


FIG. 10 is a block diagram of a power sense signal processing circuit 1000. The circuit 1000 includes a first threshold detector circuit 1010a, a second threshold detector circuit 1010b, a first comparator 1020a, a second comparator 1020b, and a summing junction 1030. The first threshold detector circuit 1010a receives a power sense signal (fundamental) 1001a and outputs a signal to the first comparator 1020a. The second threshold detector circuit 1010b receives a power sense signal (harmonic) 1001b and outputs a signal to the second comparator 1020b. The first comparator 1020a outputs a signal to the summing junction 1030. The second comparator 1020b outputs a signal to the summing junction 1030. The summing junction 1030 outputs a comparison signal 1005.

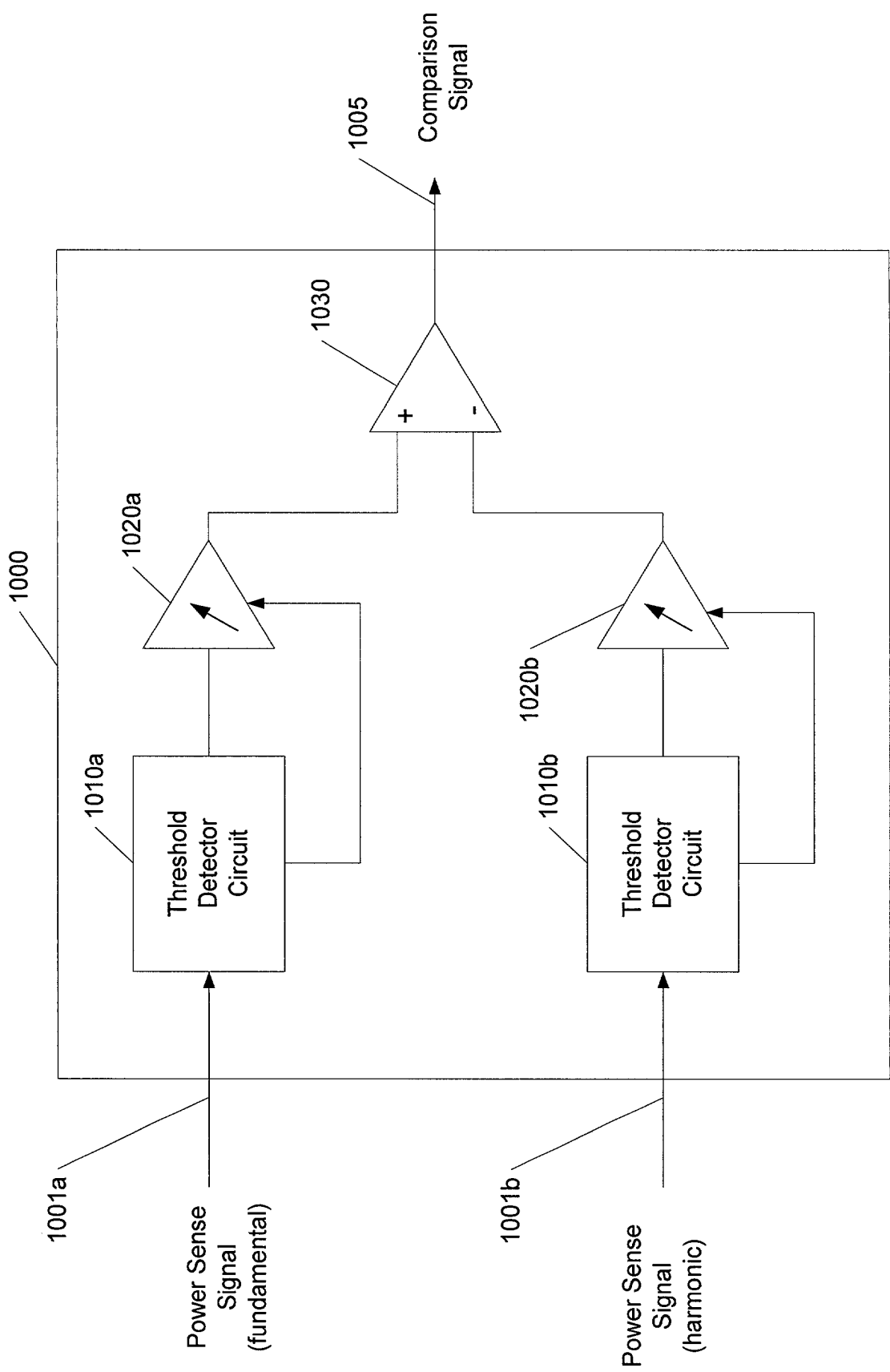


FIG. 10